

AGILENT TECHNOLOGIES, INC.
Legal Department, DL429
Intellectual Property Administration
P. O. Box 2599
Loveland, Colorado 80537-0599

PATENT APPLICATION

ATTORNEY DOCKET NO. 10030039-1

OCT 28 2003

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Brian J. Galloway, et al.

Serial No.: 10/601,993

Examiner:

Filing Date: June 23, 2003

Group Art Unit:

Title: SYSTEM AND METHOD FOR DEAD-BAND DETERMINATION FOR ROTATIONAL
FREQUENCY DETECTORS

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- (X) under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before
mailing date of first office action on the merits; whichever occurs last)
- () under 37 CFR 1.97(c) together with either a:
() Statement under 37 CFR 1.97(e), or
() a \$180.00 Processing fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- () under 37 CFR 1.97 (d) together with a:
() Statement under 37 CFR 1.97(e), and
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Please charge to Deposit Account **50-1078** the sum of \$0.00 . At any time during the
pendency of this application, please charge any fees required or credit any overpayment to Deposit
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(X) Applicant(s) submit herewith Form PTO 1449. References identified with an asterisk (*) were
disclosed in Patent Application No. _____ filed _____, now U. S. Patent No.
_____, and, as such, copies thereof are not included pursuant to the provisions of 37 CFR
1.98(d).

() A concise explanation of the relevance of foreign language patents, foreign language publications
and other foreign language information listed on PTO Form 1449, as presently understood by the
individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the
attached sheet, or where a foreign language patent is cited in a search report or other action by a
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Date of Deposit: **Oct. 23, 2003**

Typed Name: **John Pallivathukal**

Signature: [Signature]

Respectfully submitted,

Brian J. Galloway, et al.

By

David H. Tannenbaum

Attorney/Agent for Applicant(s)
Reg. No. **24,745**

Date: **Oct. 23, 2003**



PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10030039-1

SERIAL NO.

10/601,993

APPLICANT

Brian J. Galloway, et al.

FILING DATE

June 23, 2003

GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	*	DOCUMENT NUMBER	DATE	NAME
		6,055,286	Apr. 25, 2000	Wu, et al.

FOREIGN PATENT DOCUMENTS

	*	DOCUMENT NUMBER	DATE	NAME	TRANSLATION	
					YES	NO

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

		POTTBACKER, Ansgar, et al. "A Si Bipolar Phase and Frequency Detector IC for Clock Extraction up to 8 Gb/s", IEEE journal of Solid-State Circuits. Vol. 27, No.12, December 1992, pp. 1747-1751,
		RABAEY, J.M., "Digital Integrated Circuits", Prentice Hall, 1996, pp. 533-538.

EXAMINER

DATE CONSIDERED

* Copies of these references are not enclosed pursuant to 37 CFR 1.98(d). (See accompanying IDS)